

Our Reference: DFS-138-A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: Martin Gehrke & Torsten Pechstein  
Serial Number: 10/001,668  
Filing Date: October 23, 2001  
Examiner/Art Group Unit: I.B. Patel/2827  
Title: A DESIGN FOR CONSTRUCTING AN INPUT  
CIRCUIT TO RECEIVE AND PROCESS AN  
ELECTRICAL SIGNAL

COMMUNICATION

Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Entry of this communication in the above-identified application, the response time to which has been extended by a concurrently filed One-Month Extension of Time and fee, is respectfully requested